



Press Release

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Numetrics Releases Next Generation IC Design Complexity Calculation Engine for Analog, Mixed-signal and RF Semiconductor Projects

New Engine Delivers Unprecedented Accuracy of Resource and Schedule Estimates

Cupertino, Calif.: September 29, 2010 – Numetrics, the leading provider of fact-based resource and schedule estimation tools for semiconductor integrated circuit (IC) development projects, today unveiled its next generation IC design complexity calculation engine, which has been significantly enhanced for analog, mixed-signal (AMS) and RF circuit designs. The engine's advanced AMS and RF estimation models complement its existing capabilities, which include complexity modeling of system-on-chip (SoC), microprocessor, microcontroller, ASSP and ASIC designs. The new release enables engineering managers overseeing AMS/RF designs to create project staffing and schedule estimates that are far more accurate than projections based solely on intuition and traditional approaches – in a fraction of the time.

“We’re pleased to have implemented this critically important capability for our AMS and RF customers,” said Ronald Collett, Numetrics’ president & CEO. “This is an example of how our complexity calculation technology has continuously advanced during the past ten years. It’s now in its 9th generation and has been applied to over 2,000 production IC projects from more than 50 semiconductor and electronics companies.”

With its enhanced capabilities, including over 100 new parameters influencing AMS/RF design complexity, the solution comprises a library of nearly 60 widely used AMS/RF block functions, such as amplifiers, phase lock loops and filters. Each comprises function-dependent parameters that modulate the complexity estimate.

With the technology, users are able to rigorously quantify and compare the complexity of a new design to their prior projects and industry norms, a cornerstone of fact-based planning. It also enables engineering managers to accurately benchmark the execution assumptions in their project plans, which boosts schedule predictability and development productivity.

The new complexity calculation engine is embedded in the NMX-ERP™ tool suite, which includes IC Project Planner™, IC Schedule Risk Analyzer™, IC Metrics Tracker, IC DataMiner™, Multi-project Pipeliner™ and the IC Industry Benchmarking Database™. For [customer case studies](#), see:

NXP Manages Time to Market and Schedule Risk ([Download PDF](#))

and

International Rectifier: Productivity Case Study ([Download PDF](#))

About Numetrics Management Systems Inc.

Numetrics (www.numetrics.com) provides project and program managers in leading semiconductor and embedded software R&D organizations with production-proven planning tools to create project schedules and resource estimates that are far more accurate and reliable than projections based solely on intuition and traditional tools/methods—and in a fraction of the time. The enterprise resource planning (ERP) software leverages an industry database of more than 2,000 design projects to reliably measure project schedule risk and benchmark product development performance against the industry's best-in-class. The company is headquartered at 20863 Stevens Creek Boulevard, Suite 510, Cupertino, Calif. Numetrics Management Systems and NMX are trademarks of Numetrics Management Systems, Inc.