

## Press Release

### Press Contact:

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## Numetrics Webinar “Best Practices of Top-Gun Engineering Managers” Now Available Online

*Presentation and demonstration illustrate powerful ways to model IC complexity and plan resources and schedule, as used by six out of the top ten semiconductor companies.*

**Cupertino, CA – November 24, 2008** — Numetrics Management Systems, Inc., the semiconductor industry’s leading supplier of schedule predictability and risk analysis tools for IC development projects announced the availability of their online seminar covering the best practices of leading IC project planners.

The webinar includes a presentation of eight techniques used by top-gun engineering managers, followed by a demonstration of Numetrics’ NMX-ERP™ solution and IC Industry Database containing over 1200 completed IC designs from multiple industry segments.

The webinar presented the following best practices:

1. Computing IC complexity statistically
2. Estimation of resource requirements based on models
3. Rigorous “what-if” analysis for schedule / resource optimization
4. Benchmarking project execution assumptions
5. Determining the most aggressive, yet achievable project plan

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6. Quantitatively assessing the schedule / resource implications of each feature request
7. Performing root-cause analysis at the project close milestone
8. Foreseeing resource shortfalls across the project pipeline.

The demonstration showed a live application of the Numetrics toolset, through a realistic scenario involving balancing IC specification and resource availability in the context of a fixed schedule.

You may view the webinar at <http://techonline.stream57.com/numetrics/>.

For more information, please e-mail [info@numetrics.com](mailto:info@numetrics.com)

### **About Numetrics Management Systems, Inc.**

Numetrics provides a suite of software tools specifically tailored to the unique needs of the R&D organizations in semiconductor companies. The enterprise resource planning (ERP) software enables unprecedented predictability of chip development schedules, reliably measures project schedule risk and benchmarks product development performance against the industry's best-in-class. The company is headquartered at 20863 Stevens Creek Boulevard, Suite 510, Cupertino, CA. [info@numetrics.com](mailto:info@numetrics.com) [www.numetrics.com](http://www.numetrics.com). Numetrics Management Systems is a trademark of Numetrics Management Systems, Inc. All other brands or products are the trademarks or registered trademarks of their owners.